

# 6MBI100VA-120-50

**IGBT Modules** 

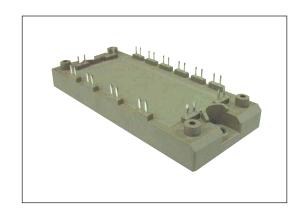
# IGBT MODULE (V series) 1200V / 100A / 6 in one package

#### **■** Features

Compact Package P.C.Board Mount Low Vce (sat)

#### ■ Applications

Inverter for Motor Drive
AC and DC Servo Drive Amplifier
Uninterruptible Power Supply
Industrial machines, such as welding machines



#### ■ Maximum Ratings and Characteristics

## ■ Maximum ratings (at Tc=25°C unless otherwise specified)

Items		Symbols	Conditions		Maximum ratings	Units		
	Collector-Emitter voltage		Vces			1200	V	
	Gate-Emitter voltage		V <sub>GES</sub>			±20	V	
	Collector current		Ic	Continuous	Tc=100°C	100		
			Icp	1ms	Tc=80°C	200	^	
			-lc			100	Α	
			-lc pulse	1ms		200		
	Collector power dissipation		Pc	1 device		520	W	
Junction temperature			Tj			175		
Operating junciton temperature (under switching conditions)			Tjop			150	°C	
Ca	Case temperature		Тс			125		
Storage temperature		Tstg			-40 to +125			
Iso	lation voltage	between terminal and copper base (*1) between thermistor and others (*2)	Viso	AC : 1min.		2500	VAC	
Sc	rew torque	Mounting (*3)	-	M5		3.5	N m	

Note \*1: All terminals should be connected together during the test.

Note \*2: Two thermistor terminals should be connected together, other terminals should be connected together and shorted to base plate during the test.

Note \*3: Recommendable value: 2.5-3.5 Nm (M5)

#### ● Electrical characteristics (at Tj= 25°C unless otherwise specified)

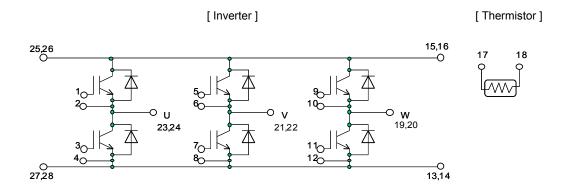
Items		Cumbala	Conditions		Characteristics			Units
		Symbols			min.	typ.	max.	Units
	Zero gate voltage collector current	Ices	V <sub>GE</sub> = 0V, V <sub>CE</sub> = 1200V		-	-	1.0	mA
	Gate-Emitter leakage current	Iges	V <sub>GE</sub> = 0V, V <sub>GE</sub> = ±20V		-	-	200	nA
	Gate-Emitter threshold voltage	V <sub>GE (th)</sub>	V <sub>CE</sub> = 20V, I <sub>C</sub> = 100mA		6.0	6.5	7.0	V
	Collector-Emitter saturation voltage	V <sub>CE (sat)</sub> (terminal)	V <sub>GE</sub> = 15V I <sub>C</sub> = 100A	Tj=25°C	-	2.20	2.65	V
				Tj=125°C	-	2.50	-	
				Tj=150°C	-	2.55	-	
		V <sub>CE (sat)</sub> (chip)	V <sub>GE</sub> = 15V I <sub>C</sub> = 100A	Tj=25°C	-	1.75	2.20	
				Tj=125°C	-	2.05	-	
				Tj=150°C	-	2.10	-	
	Internal gate resistance R <sub>9</sub> (int)		-		-	7.5	-	Ω
ē	Input capacitance	Cies	V <sub>CE</sub> = 10V, V <sub>GE</sub> = 0V, f = 1MHz		-	9.1	-	nF
Inverter		ton	$V_{\rm CC}$ = 600V $I_{\rm C}$ = 100A $V_{\rm GE}$ = +15 / -15V $R_{\rm G}$ = 1.6 $\Omega$		-	0.39	1.20	μs
=	Turn-on time	tr			-	0.09	0.60	
		tr (i)			-	0.03	-	
	Turn-off time	toff			-	0.53	1.00	
	Turn-off time	tf			-	0.06	0.30	
		V <sub>F</sub> (terminal)	I <sub>F</sub> = 100A	Tj=25°C	-	2.30	2.75	- V
	Forward on voltage			Tj=125°C	-	2.55	-	
				Tj=150°C	-	2.50	-	
		V <sub>F</sub> (chip)	I <sub>F</sub> = 100A	Tj=25°C	-	1.85	2.15	
				Tj=125°C	-	2.10	-	
				Tj=150°C	-	2.05	-	
	Reverse recovery time	trr	I <sub>F</sub> = 100A		-	-	0.35	μs
tor	Pacietones	R	T = 25°C		-	5000	-	Ω
Thermistor	Resistance		T = 100°C		465	495	520	
그	B value	В	T = 25 / 50°C		3305	3375	3450	K

#### • Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
items		Conditions	min.	typ.	max.	Ullits
Thermal resistance (1device)	Rth(j-c)	Inverter IGBT	-	-	0.29	°C/W
Thermal resistance (Tuevice)		Inverter FWD	-	-	0.55	
Contact thermal resistance (1device) (*4)	Rth(c-f)	with Thermal Compound	-	0.05	-	

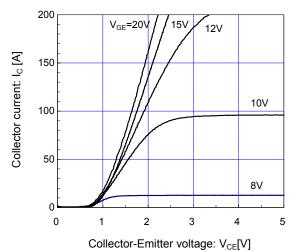
Note \*4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

## **■** Equivalent Circuit Schematic

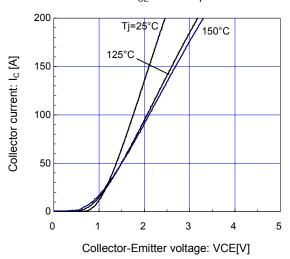


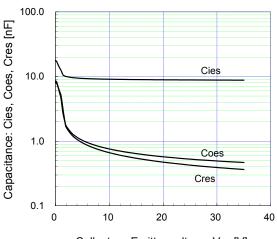
#### ■ Characteristics (Representative)

 $\label{eq:continuous} \begin{tabular}{ll} \mbox{Inverter } \mbox{]} \mbox{Collector-Emitter voltage (typ.)} \\ \mbox{Tj= } 25^{\circ}\mbox{C / chip} \end{tabular}$ 



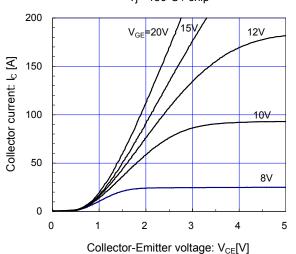
 $[Inverter\ ] \\ Collector\ current\ vs.\ Collector-Emitter\ voltage\ (typ.) \\ V_{GE} = 15V\ /\ chip$ 



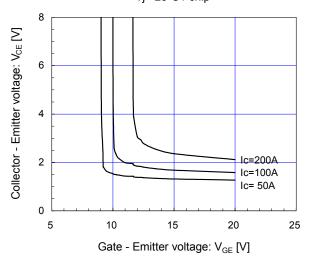


Collector - Emitter voltage:  $V_{CE}$  [V]

[ Inverter ] Collector current vs. Collector-Emitter voltage (typ.)  $Tj = 150^{\circ}C / chip$ 



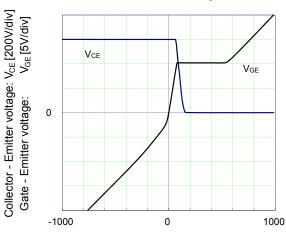
 $\label{eq:continuous} \begin{tabular}{ll} \b$ 



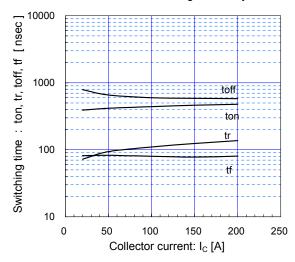
[Inverter]

Dynamic gate charge (typ.)

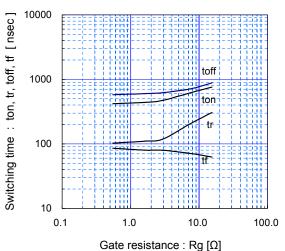
Vcc=600V, Ic=100A, Tj= 25°C



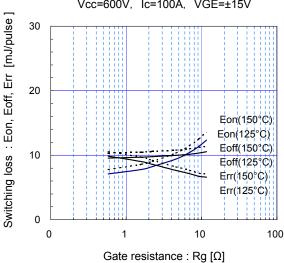
[Inverter] Switching time vs. Collector current (typ.) Vcc=600V, VGE= $\pm$ 15V, Rg= $1.6\Omega$ , Tj=  $125^{\circ}$ C



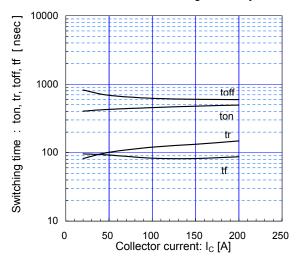
[ Inverter ]
Switching time vs. gate resistance (typ.)
Vcc=600V, Ic=100A, VGE=±15V, Tj= 125°C



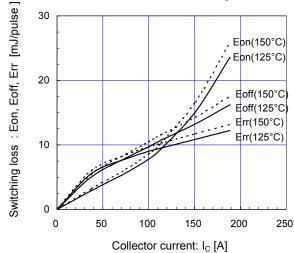
[ Inverter ]
Switching loss vs. gate resistance (typ.)
Vcc=600V, Ic=100A, VGE=±15V



[ Inverter ] Switching time vs. Collector current (typ.) Vcc=600V, VGE= $\pm$ 15V, Rg= $1.6\Omega$ , Tj=  $150^{\circ}$ C



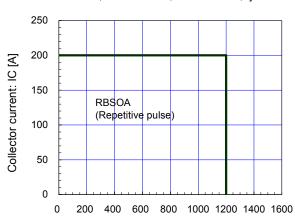
 $[Inverter] \\ Switching loss vs. Collector current (typ.) \\ Vcc=600V, VGE=\pm15V, Rg=1.6\Omega$ 



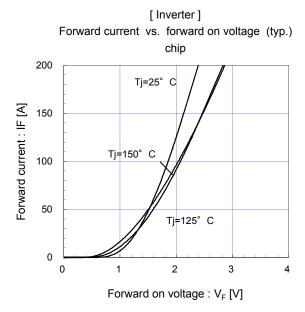
[ Inverter ]

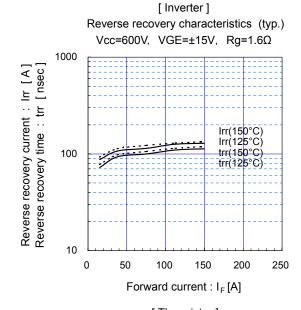
Reverse bias safe operating area (max.)

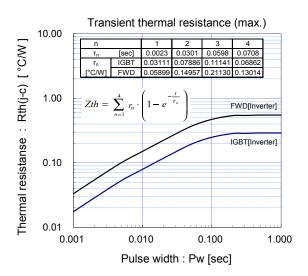
+VGE=15V,-VGE <= 15V, RG >=  $1.6\Omega$ ,Tj =  $150^{\circ}$ C

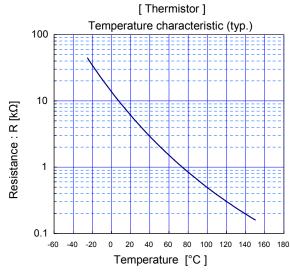


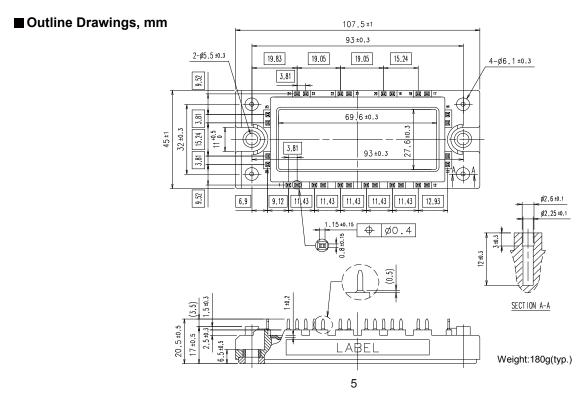
Collector-Emitter voltage : V<sub>CE</sub> [V] (Main terminals)











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